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## APPLICANTS

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\*\* FOREIGN APPLICATIONS \*\*\*\*\*  
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35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no Met after Allowance Verified and Acknowledged <u>Stanley S. Panchajanya</u> Examiner's Signature Initials				

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## TITLE

TEMPERATURE DETECTION FROM DIFFERENCES IN OFF LEAK CURRENTS OF NMOS AND PMOS TRANSISTORS ON CPU CHIP

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